



Power Device Packaging

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Project ID: APE023

This presentation does not contain any proprietary, confidential, or otherwise restricted information

Overview

Timeline

Start Date: Oct. 2009

End Date: Sept. 2014

70% Complete

Budget

DOE Share – 100%

FY10 received: \$480K

FY11 received: \$650K

FY12 received: \$672K

Barriers

- Existing inverters are twice the cost of the DOE 2020 target. Power modules make up 50% of the inverter's cost
- State-of-the-art (SOA) technologies have limitations in electrical, thermal, and reliability performance, as well as high manufacturing costs.

Targets:

 40% cost reduction and 60% power density increase of the power module, in line with DOE power electronics 2020 targets.

Partners

- ORNL Team Members: Puqi Ning, Andy Wereszczak, Randy Wiles, Laura Marlino
- The University of Tennessee: Fred Wang



Objective

The fundamental efforts of this project are to:

- <u>Identify</u> the limitations and shortcomings with existing device packaging approaches;
- <u>Develop</u> new packaging concepts for improved electrical, thermal performance, manufacturability, and reliability;
- <u>Complement</u> other power electronics research efforts within the Vehicle Technology Program.

FY12

- Evaluate industry SOA power modules: Packaging performance, material, processing and structure analysis.
- Planar_Bond_All (PBA) power module development
 - ➤ Packaging process optimization: Material/structure, cost-effectiveness manufacturing.
 - ➤ Prototype module fabrication: Integration of advanced processing techniques.
 - ➤ Module testing and analysis: Electrical, thermal, properties measurement and analysis.
- **Provide packaging support for other VTP APEEM projects:** Fabrication of customer-specific power modules.

Milestone

- Sept. 2011
 - Selected advanced packaging candidate technologies.
- Go No/Go Decision Point: Determine if prototype PBA technology could potentially meet the performance and power density targets.
- Sept. 2012
 - -Test PBA modules to confirm improvements in electrical and thermal performance.
 - -Provide prototype modules for Segmented Drive Inverter Project (APE004) and Wide Bandgap Project (APE003).
- Go/No Go Determine if PBA modules can meet the targets on cost and reliability.



Approach

Overall Strategy

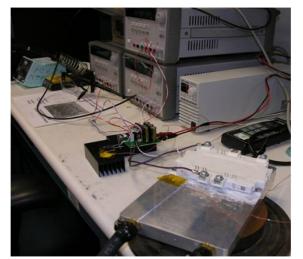
- Benchmark existing power device packaging technologies, including package configuration, materials characterization, processing, and thermal and electrical performance evaluations. Determine the "weak links".
- <u>Develop</u> new packaging approaches through simulation and experiments to improve power module electrical, thermal, thermo-mechanical performance and manufacturing costeffectiveness.
- Apply packaging expertise to provide customer-specific power modules to VTP projects.

Status of Milestone

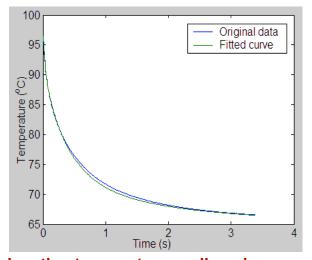
- FY11 PBA prototype completed
- FY12 PBA optimized module fabricated; Testing is underway;
 - Customer modules delivered.



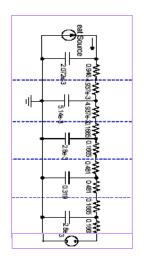
Benchmark: Thermal Performance Characterization of SOA Modules



Infineon[®] HybridPack1™ under test

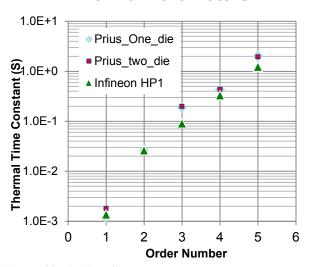


Junction temperature cooling down curve

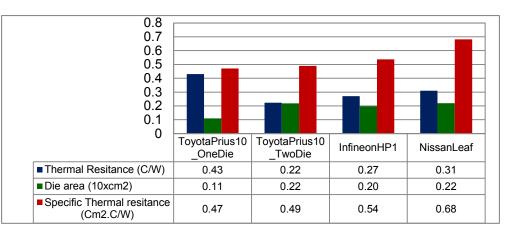


Thermal model

Thermal Parameters

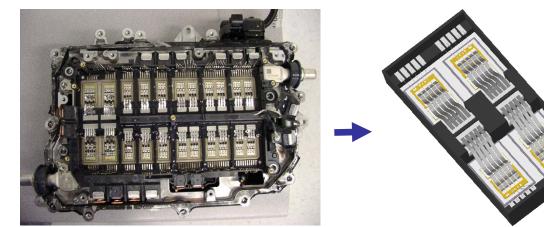


Thermal Resistance Comparison





Benchmark: Electrical Performance Characterization of SOA Modules



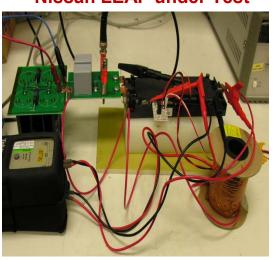


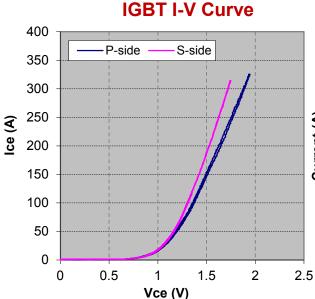
Positive **3** 7.3 nH 6.5 nH **≨** 0.18 mΩ **€** 0.17 mΩ 19.6 nH $7 \text{ m}\Omega$ 11.3 nH 14.4 nH $0.44 \text{ m}\Omega$ $0.33~\text{m}\Omega$ Neutral 6.5 nH 7.3 nH = 0.18 mΩ $0.17~\text{m}\Omega$ ₩ww 19.6 nH 14.4 nH 7 mΩ 11.3 nH $0.44\ m\Omega$ $0.33 \ m\Omega$ Negative

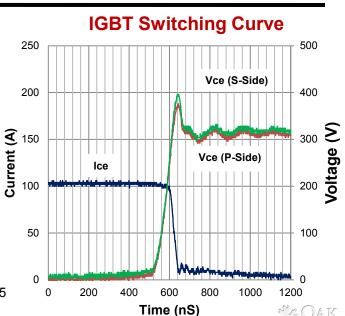
Lump element model of parasitic electric parameters

Nissan LEAF under Test

Prius 2010 Module



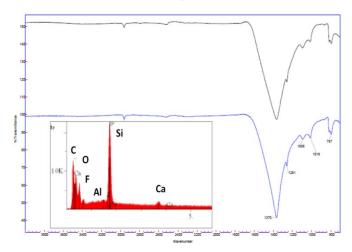




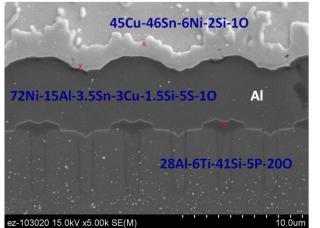
Benchmark: Micro-structural analysis of packaging structures/materials



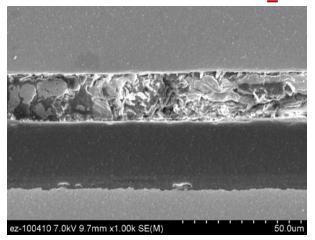
Die attach solder layer in Infineon HP1



Insulator sheet in Nissan LEAF™ module



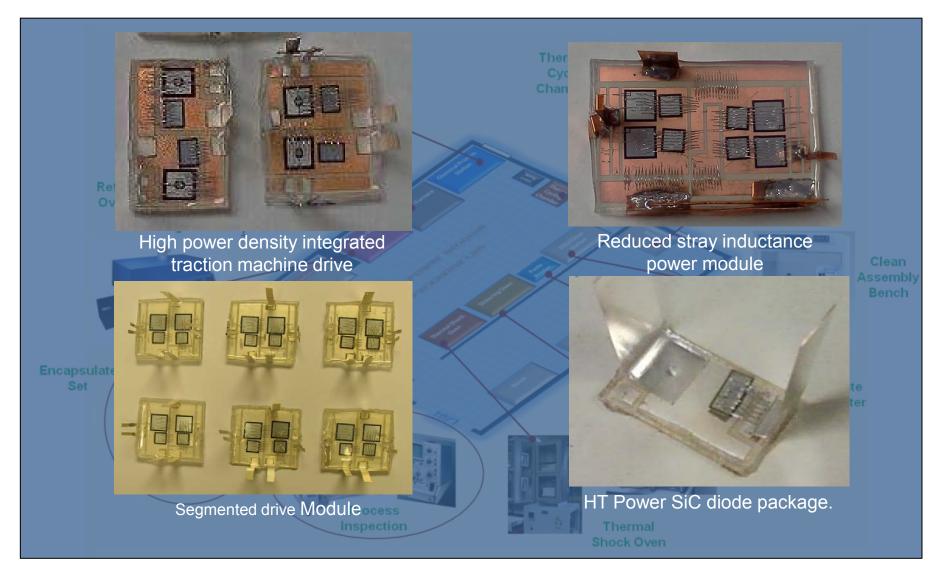
Metallurgical compositions on top of IGBT die in Mitsubishi TPM_II



Polyimide bond line

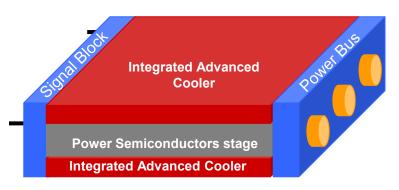


Prototype: Customer-specific power modules





Develop: Planar_Bond_All power module



❖3-D planar Electrical Interconnection ❖Symmetrically Mechanical Structure

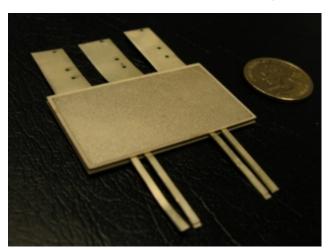
❖Integrated, Double Sided Cooling **❖**Cost-effective Manufacture



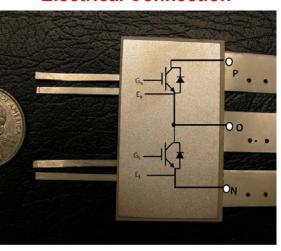
Advanced Power Module Concept

Planar Bond All Power Module Prototype

Planar Bond Power Stage

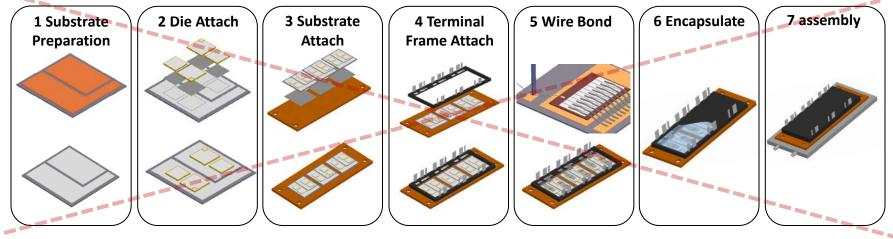


Electrical Connection



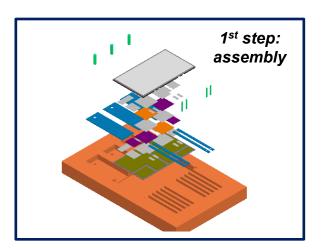


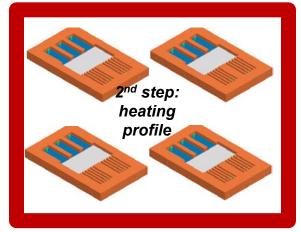
Develop: Advanced manufacturing process

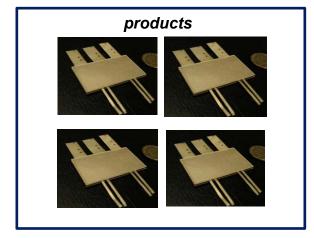


Wire Bond Packaging

Planar Bond All



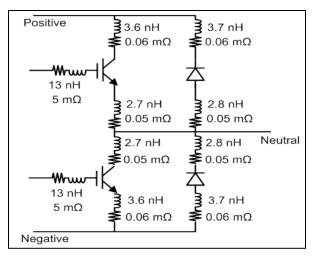




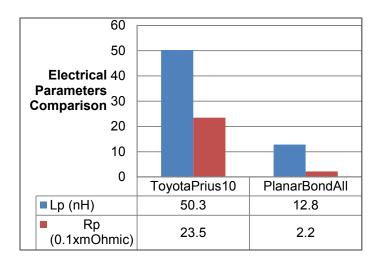
Patent Pending: serial number 61/509312



Develop: Analysis of electrical performance of PBA module

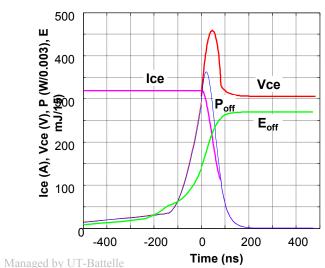


Lump element model of PBA module

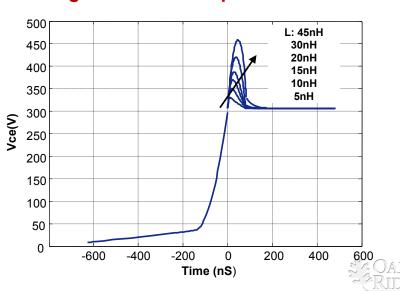


Parasitic parameters comparison

Simulation of IGBT switching



Voltage overshoot vs. parasitic inductance



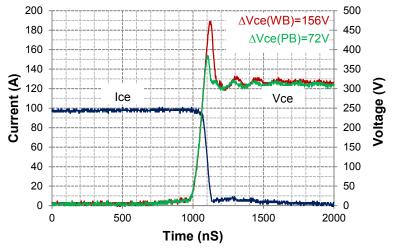
Develop: Analysis of electrical performance of PBA module





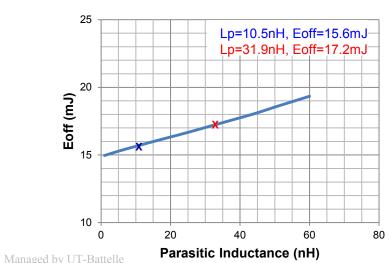
Inductance (nH)	Experimental Value	Calculated Value
Planar Bond_Lower IGBT	10.5	6.3
Wire Bond-Lower IGBT	31.9	23.5

Comparison: PBA vs. wire bond module

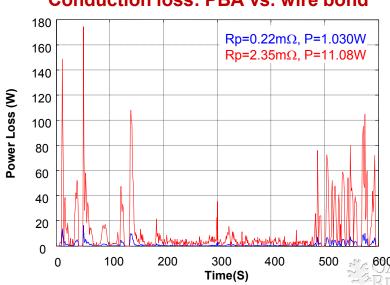


Voltage overshoot: PBA vs. wire bond

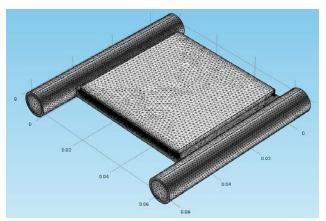
Switching loss: PBA vs. wire bond



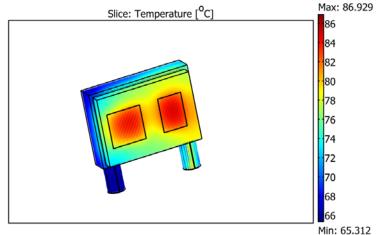
Conduction loss: PBA vs. wire bond



Develop: Analysis of thermal performance of PBA module

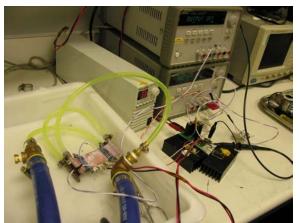


3-D Thermal Model of PBA module with mini-cooler



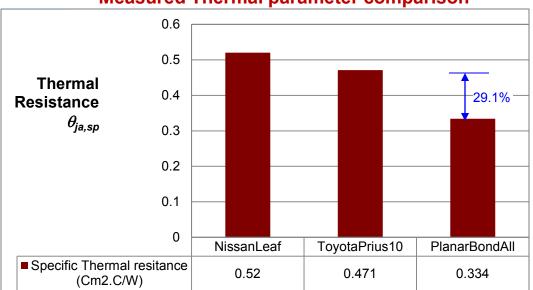
Temperature distribution with cooling

PBA module under thermal test



$$\frac{\$}{kW} \propto \frac{S_{Die\,Area}}{P} = \frac{\eta \bullet \theta_{ja,sp}}{(T_i - T_a)}$$

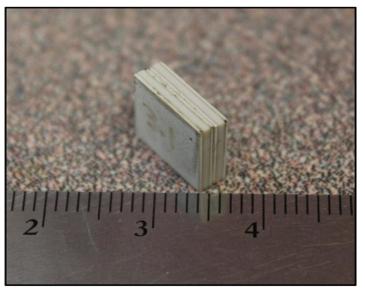
Measured Thermal parameter comparison





Develop: New bonding material and processing

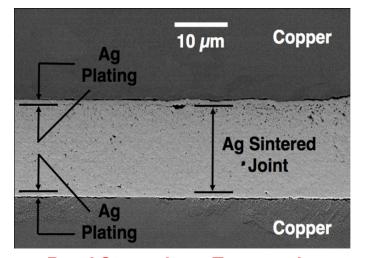
Ag Bonded DBC Substrates



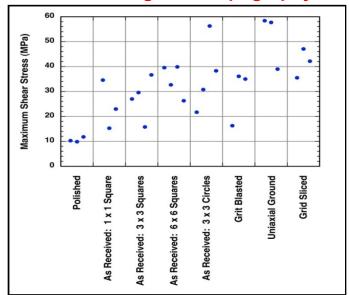
Bond Line View After Tear Down



Cross sectional View of Bond Line



Bond Strength vs. Topography





Collaboration and Coordination

NREL

- Will use ORNL supplied modules to develop reliability parameters
- ORNL Materials Science and Technology Division
 - Leveraged DOE VTP Materials Program
 - Coordinated research to address packaging materials needs
- University of Tennessee
 - Assisted in benchmarking commercial packages
- Virginia Tech University
 - Collaborated on die attach material and power electronics module packaging



Future Work – FY12

- Complete development of PBA processing portfolio
 - Complete the die top electrode design and fabrication process;
 - Complete the insulation processing for solderable front metal (SFM) power semiconductor switches;
 - Assemble all individual steps into one processing run;
 - Finish the electrical and thermal performance tests.
- Continue to benchmark packaging technologies (performance, materials and processes)
- Continue to support new power electronics module development



Future Work - FY13 and Beyond

- Enhance reliability of PBA concept
 - Perform thermo-mechanical design and simulation of advanced planar bond module packages;
 - Implement cost-effective materials and structures into PBA power modules;
 - Conduct simulations and preliminary reliability tests of packages.
- High temperature module packaging development
 - Incorporate advanced bonding material/processing techniques;
 - Investigate new encapsulate, thermal materials;
 - Evaluate the high temperature effects of power module on cost, efficiency and reliability, including Si, SiC and GaN power semiconductors.
- Continue to benchmark SOA technologies as needed (module performance, materials evaluation and processing)
- Provide packaging support for other projects



Summary

- Developed PBA power module packaging technologies and fabricated power modules resulting in,
 - Decreased package thermal resistance by 30%;
 - Decreased package parasitic electrical inductance by 3/4th, and electric resistance by 90%;
 - Reduced the major packaging manufacturing steps from five (5) to two (2);
 - Achieved more than 30% volume, and weight reduction.
- Benchmarked SOA automotive power module
 - Nissan LEAF module: electrical, thermal test, insulator sheet analysis;
 - Toyota Prius 2010 module: thermal, and microstructure;
 - Infineon HybridPack1: thermal, and microstructure;
 - Mitsubishi TPM II: semiconductor and package microstructure.
- Delivered customer-specific power modules,
 - Segmented drive inverter modules (FY12);
 - HDITMD inverter modules (FY11);
 - High power SiC diode modules (FY12);
 - Low inductance wire bond modules (FY11).

